PRATIK BHOYAR

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PROFESSIONAL SUMMARY

Embedded Software Developer | 5+ Years of Experience

- Software Development: Over 5 years of experience in embedded software development with a focus on C & C++.
- Technical Expertise: Strong proficiency in GHS and QNX systems. Knowledge of CAN and UDS protocols and a solid understanding of Diagnostic and Camera Manager modules.
- Key Accomplishments: Developed and implemented multiple APIs using Database Management (DBM) for communication via Virtual Address Space.
- Methodologies & Tools: Experienced in the Vmodel development lifecycle and highly proficient in Git for version control.
- Domain Knowledge: Familiar with automotive and networking embedded domains.

PROJECT EXPERIENCE

Company: Deevia Software India Pvt LTD (Jan 2022 – Jan 2025)

Role: Senior Software Engineer Client: Valeo India Pvt Ltd

Project 1: Ultrasonic Camera Assistant Parking (BMW) (Jan 2022 - Aug 2023)

Responsibilities:

- Worked on Diagnostics Module for RPU to APU communication using ICOM on GHS RTOS platform.
- Implemented DBM for storing car records shared between 2 SoCs (M3N, V3H) in C++.
- Analysed traces of required features using Carman tool.
- Implemented multiple DBM records for Diagnostics.
- implemented some Diagnostic Module for extension of project on QNX OS.

Project 2: Volkswagen TVemid (Sept 2023 - Aug 2024) Responsibilities:

- Designed a Camera Communication Control Manager VAS module for ECU-to-camera communication in C++ on GHS RTOS platform.
- Developed a handshake mechanism to establish initial connections using custom UDP-based messages (camera heartbeat, ACKs/NACKs).
- Provided DBM records using wrappers provided

- by GHS RTOS to store camera state, production data, and MAC addresses.
- Developed multiple API interfaces to send commands to cameras, perform request and response operation.

Project 3: Porsche SV62 One Box (Sept 2024 – Jan 2025) Responsibilities:

- Implemented a Camera State Machine in C++ on QNX platform.
- Developed multiple API to communication between ECU and Camera to get the requested data.

Company: Mirafra Technologies (July 2018 - July 2020)

Role: Software Engineer I Client: Intel India Pvt Ltd

Project 4: GRX550 Intel Home Division Router (Jan 2019 - July 2020)

Responsibilities:

- Worked on L2TP and IPsec application software in C on Openwrt OS platform.
- Analysed traces of required features using Wireshark.

TECHNICAL SKILLS & TOOLS USED

- C++11/14 (3 years)
- C (2 years)
- GHS RTOS (2 years)
- QNX (1 year)
- XDS Debugger (1 year)
- GHS Probe Debugger (2 years)
- UDS (1.8 years)
- CAN (3 years)
- Git (5 years)
- CANoe (3 years)
- Track and Release (Core ALM & JIRA) (5 years)
- DOORS (5 years)
- Openwrt (1.5 years)

EDUCATION

KITS, Ramtek (RTMNU)

Bachelor of Engineering in Electronics -2016

ACHIEVEMENTS & CERTIFICATIONS

- Team Awesome Award for team support in critical tasks.
- Certified Embedded Professional from Vector India, Hyderabad.

LANGUAGES KNOWN

• English, Hindi, Marathi