

# Suryakant Namdeo Targolkar.

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## **Objective:**

An Embedded Software Engineer having around 10+ years of experience in Embedded Domain and 8 years in Automotive domain, worked on Climate Control ECU in vehicles. Currently looking for the better opportunity where I can improve my skills which are required to handle any situations at runtime.

## **Education:**

- Completed Post Graduate Diploma in Embedded and VLSI system design in 2011 from CDAC, Pune.
- Completed Bachelor's degree in Electronics and Telecommunication in 2010 from Shivaji University.
- Completed Diploma in Electronics and Telecommunication in 2006 from Government Polytechnic Pune, (Autonomous University).
- Secondary Schooling Certificate (10th Standard) from Maharashtra State Board with Distinction in 2003.

## **Experience Summary:**

### **Company:**

- **KPIT Technologies , Pune ( Nov 2023 to Till Date)**

### **Subject Matter Expert**

- Create Static and Dynamic diagrams using EA Tool and Visio Tool according customer requirements
- Create detailed data flow diagram using VISIO tool according customer requirements.
- Worked as customer interface for Front and Rear Light feature for OEM.

- **Wipro Ltd ( May 2023 to Oct 23)**

### **Technical lead**

- Handling customer requirement for Low Beam Feature
- Updated/Implemented feature according to Requirements.

- **BHTC India Pvt Ltd (Oct 2016 to Feb 2023)**

### **Autosar Software Developer:**

- ☐ Ask the support team to create project structure on MKS and on Doors, which is used for further activity.
- ☐ Whenever new SIP come from Vector/Customer, maintain it on MKS repository and some BHTC component into SIP for further use.
- ☐ Taking requirement from the entire competency for initial project setup.
- ☐ Depend on schematic and signal catalogue, doing initial board bring up activity.
- ☐ Update linker script, startup file and project related batch file.
- ☐ Integration of MCAL into SIP and update SIP in MKS repository.
- ☐ Autosar configuration for MCAL component like MCU, DIO, PORT, SPI, FEE, PWM, WDG using Da-Vinci configurator
- ☐ Configuration OS.BSW, ECUM components as per project using Da-Vinci configurator
- ☐ Configuration CAN related components using Da-Vinci Configurator.
- ☐ Configuration memory related components using Da-Vinci Configurator.
- ☐ Configuration SBC component using Da-Vinci Configurator.
- ☐ Creation of task in OS using Da-Vinci configurator and testing of it.
- ☐ Dummy port creation for test purpose.
- ☐ Testing of each component (Bench test or using PICO scope).
- ☐ Update the test cases on Doors and revision of files on MKS.
- ☐ Review the test cases from quality person and update reviewed documents on project portal.

#### **CAN Software Developer:**

- ☐ Working as a CAN communication protocol developer
- ☐ Written code in Embedded C for communication between different ECUs in network
- ☐ Software requirement analysis of CAN, Fine design and Low-level test creation.
- ☐ Working on different project platform such as Autosar, non-autosar etc
- ☐ Experience on Vector CAN stack, Mentor graphics stack for autosar as well as non- autosar projects
- ☐ Configuration of CAN Stack for autosar projects, configuration in the Vector Geny tool for non-autosar projects.
- ☐ Creation and implementation of DBC based on the customer's database.
- ☐ Implementation and validation of CAN Tx/Rx signals with debugging etc.
- ☐ Implemented CAN Self Diagnostic and Network Manager for CAN bus.

#### **BAP Software Developer:**

- ☐ Working as a **BAP** protocol developer for VW OEM's.
- ☐ Written code in Embedded C for communication between different ECUs in network.
- ☐ Configuration of BAP Stack using BAP Gen tool
- ☐ Implementation and validation of BAP signals with debugging using **V-Test Studio**.

#### **Projects Managed:**

- ☐ German OEMs: VW, Porsche, Bentley
- ☐ Chinese OEMs: Greely, SAIC, Great Wall Motors.
- ☐ INDIA OEMs: TATA, Mahindra, Vinfast.

#### **• ETS PUNE (JAN 2016 to SEP 2016)**

##### **Embedded Design Engineer:**

##### **Project1–OBD: on board diagnostics -2- CAN communication**

- ☐ Microcontroller: LPC2129.
- ☐ Role: Software Developer.
- ☐ Responsibilities:
  - Design and implementation of 2 microcontroller communication.
  - Designs of software download procedure for both controllers.

#### **• N-Sys Technologies Pune (June 2012 to Oct 2015)**

##### **Embedded Design Engineer:**

##### **Project1–Vehicle tracking system based on GPS and GPRS.**

##### **Role And Responsibilities:**

- Design PCB using PROTEUS.
- Embedded application developer. Handling full project development (directly requirement from customer) and integration with server and various PC based tool.
- Handling of interface with various sensors fuel sensor, temperature sensor, RFID, Camera, MotionSensor.

##### **Project2–Wireless Electricity Meter(ZIGBEE)**

##### **Role And Responsibilities:**

- Embedded application developer.
- Application development for RF module on Electricity Meter.
- Implement various customer requirements.

### **Project3– CAN Communication**

Team Size:1 Microcontroller::LPC2129

Tools:

Compiler: KEIL vision, Dock light

Languages and standards: C.

#### **Role And Responsibilities:**

- Board design using Proteus software
- Embedded developer. Application development for board to board communication.
- Established communication between 2 microcontrollers.
- Handling of interface with various sensors.
- Implement various customer requirements.

#### **Skills:**

- Good knowledge of C programming.
- Competency in the Embedded C programming language.
- Basic knowledge of UART communication protocols.
- Ability to understand customer requirements

#### **Tools used:**

- Followed 'V' mode for a Software development life cycle
- Used DOORs, Change management, MKS for project management
- Used VISIO and EA Tools for requirement
- Used D-Oxygen Tool for Document merging
- Used PC-Lint and Polyspace tool to solve MISRA C errors
- Worked on Renesas RH850, RL78.
- Compilers: IAR, Multi (GHS),
- Familiar with Logic Analyser for a communication protocol analysis.
- BAP Gen Tool and V-Test Studio for BAP Modules

#### **Personal Details:**

- Father Name : Namdeo Y. Targolkar
- Date of Birth: 14/11/1987
- Nationality: Indian
- Marital Status: Married
- Languages known: English, Hindi, Marathi,

I hereby declare that the above information is true to my knowledge.

Date:

Place:

Sign:

Suryakant Targolkar