

# DEEPAK YADAV

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## TECHNICAL SKILLS

- **Core Competencies:** Technical Product R&D, Product Roadmaps, Test Driven Development, Team Leadership, QA Testing, SWOT Analysis, UX/ UI Design, Market & Competitor Research, Go-To-Market Launch Plans, Requirement Gathering & Analysis, Product Lifecycle Development, Troubleshooting & Debugging, Financial Modelling & Forecasting.
- **Tools & Framework:** Agile Methodologies, Scrum, Confluence, Slack, Atlassian JIRA, Google Analytics, Databases, Visual Studio Code, Visual Studio 2017/19, Simulink, Xilinx ISE.
- **Testing Methodologies:** White Box testing, Black Box Testing, Stress testing, Regression testing, Manual Testing, Smoke testing.
- **Languages:** C/C++, HTML/CSS, MATLAB.

## PROFESSIONAL EXPERIENCE

### Western Digital | Staff, Systems Design Engineer

June'2022– Present

- Worked as part of Removable product group's system design team (SD/uSD) with focus on designing and development of new features to meet continuous demand of increasing capacity and performance (+40% increase YOY).
- **Managed the entire product life-cycle** from conception, requirements definition, development, testing, launch and maintenance, with frequent client communication and adjustment along the way (+20% increase in TAM).
- Conduct on-going market research & competitive analysis to process in-depth knowledge of markets, procedures, & products.
- **Collaborated with the global sales team and marketing management** on the development and implementation of strategic and tactical plans to ensure profitability and growth for all assigned product/procedural areas.
- **Delivered the 1.5TB capacity uSD card** (highest capacity in smallest form factor) while working through different challenges of thermals, system performance and compatibility issues (10x increase in sales and production volume).
- **Defined and analyzed product metrics** that helps the growth of the product (+25% improvement).
- Provided feasibility analysis, estimations, and generated requirements list for next gen uSD controller taking into consideration power, performance, and cost.
- **Manage external test laboratories** to conduct tests to achieve certifications by specifying the standards and applicable tests, preparing test samples, providing product information, and approving test reports.
- **Communicated product vision, roadmap and releases to the Sales, Client Success, Marketing, and Leadership team.**

### Keysight Technologies | RnD Engineer II

Apr'2018– 2022

- Being a part of Keysight Technologies, have a good understanding of test and measurement equipment.
- **Defining product vision, portfolio and value proposition, roadmap** and leading all activities leading necessary to deliver product in the market (design, testing, release and demo), added +5 products in the portfolio.
- Worked closely with the development team in US and Germany to test and maintain the firmware, SFP, SCPI API, hardware drivers and the signal quality of the instruments with leading 5 members in the team.
- **Tracked software validation activities and results**, confirmed that software complied with client specifications. Prepared summaries of validation testing results.
- Drive implementation of test and validation processes throughout our NPI lifecycle in all functional areas, e.g. mechanical, electronic and software.
- **Assist with the establishment of validation standards**, Identify and track faults within software during product life cycle, implement procedures for effectively debugging software glitches.
- Responsibilities include but are not limited to creating functional and GUI based test plans, execution of weekly regression tests, executing performance tests, defect reporting.

## EDUCATION

### B.E. in Electrical & Electronics | Delhi Technological University (formerly DCE)

May'13-Jun'17

**Coursework:** System Design & Architecture, Artificial Intelligence & Machine Learning, Software Development Life Cycle, Economics, Microcontroller, Microprocessors, Programming Languages.

## Patent Applications

- US Patent 2022-INV-0001665 **"Flash Memory Read Scrub with Minimized Read Amplification"** Nov 03, 2022  
Inventors: Deepak Yadav, Sai, Ankit
- US Patent 2023-INV-0001850 **"Stream Detection with Minimized Read Amplification"** Feb 10, 2023  
Inventors: Aayush Jain, Deepak Yadav