AKHIL BHARTIYA

Södertälje Sweden

+46-0767473497/+91-8805440651

15172

akhilbhartiya123@gmail.com

OBJECTIVE

To keep myself well equipped with the latest technologies emerging in the current world and to use them in an innovative manner, working towards the development of the esteemed organization with committed and dedicated people to get a rock-solid foundation to face the constantly changing world.

EXPERIENCE SUMMARY

My experience includes over 12+ years of Software application Analysis, Design &Development, C/C++(11,14,17), Adaptive-Autosar, GPS/GNSSData Processing, SomeIPProtocol, CANProtocol(J1939), UDSOnCan(J19 39 ISO14229), DoIPClient(ISO 13400-2019), OpenGL 4.5, CAA, Unix, Pro*c. Currently involved in RTOS System's Application Analysis, Design, Development and Defects Fixing. Involved was in designing High Performance applications which includes implementations in technologies like INTER PROCESS COMMUNICATION(IPC), Client–Server TCP/IP Protocol, Multithreading application, System Migration. Having strong analytical problem-solving skills and business processes.

JOB PROFILE

CURRENTLY WORKING IN LTTS as Technical Manager.

Organization Details	Larsen & Toubro Technology Services
Clients Details	Scania (SWEDEN) Oct 2022-current
Projects Connected System	
Connected Services make full of smart technology, advanced sensors and wireless	

connected Services make full of smart technology, advanced sensors and wireless connectivity.it means that hundreds of thousands of connected vehicles and engines in use all over the world-providing data creates improved engineering as well as business. Under this vehicle performance, fleet position ,vehicle alarm ,Smart tachograph(**DTCO**) download & driver evaluation etc. are being measured all the time.

Role & Responsibilities

- Technical Analysis, Design & Development.
- System architecture design.
- Code review & Functional Architecture .
- HIL & SIL Adoption for the integration Testing
- Test Rig/Hardware Simulator Setup for End to End for Software Testing.
- Flashing QNX(RTOS) and Linux in C400 ECU/Secure Update using VCI4 (DoIP).

Environment:-

C++11/14,Adaptive Autosar(**VECTOR**), GTEST, GMock, TCP/IP(Sockets Programming), CAN,UDSonCAN,SOME-IP,DoIP, Multithreading, QNX(RTOS),CAN Analyzer(1630 A),Canoe 5620,VCI2,3,J1939 Heavy Vehicle ,Sensors API, Google Protobuf,Python,Docker ,VSCode,DS1990A,GIT & Others CI-CD tools,**AUTOMOTIVE Enthernet**

Siemens

Clients Details	Siemens Mobility (Jun-2020,Oct-2022)
Projects-1	Unattended Track Geometry Measurement

UGMS track recording systems are fitted to service trains, track managers and engineers have access to frequent track geometry data that accurately reveals the rate of track deterioration at any location, provides feedback on the quality of track maintenance and renewal work and provides warning of any sudden change in track quality which might be a cause for concern.

- Role Technical Analysis & Development Responsibilities
 - Technical Analysis, Requirement Gathering.
 - Prepared architecting solutions for system architecture design.
 - Developed, implementation, testing, design, analysis.

Environment

C/C++, TCP/IP, Multithreading, QNX, Boost-C++, Python, Sensors Api, OpenCv ,IR Camera,Google Protobuf

Projects-2	Broken Rail Detection

Broken rails are a primary cause of train derailments resulting from track infrastructure faults. New technology that allows direct detection of broken or severely cracked rail by equipment mounted on-board revenue trains transforms the way that rail failures can be detected and reported. This technology provides the potential to enhance the safety and operations of a railroad by overcoming limitations in the performance of current detection technologies and support new signaling technologies.

Role – Technical Analysis & Development Responsibilities

- Technical Analysis, Requirement Gathering.
- Prepared architecting solutions for system architecture design.
- Developed, implementation, testing, design, analysis

Environment

C/C++, TCP/IP, Multithreading, QNX, Boost-C++, Python, Sensors Api, Google Protobuf, OpenCv , IR Camera

Project Details	Dassaults Systemes
Clients Details	DS(Jul-2016, Jun-2020)

ENOVIA provides a framework for collaboration for Company's PLM software. It is an online environment that involves creators, collaborators and consumers in the product lifecycle.

Role – Technical Analysis & Development Responsibilities

- Technical Analysis, Requirement Gathering.
- Prepared architecting solutions for system architecture design.
- Developed, implementation, testing, design, analysis. Environment.

C/C++/VC++, CAA, OPEN GL, Digital Image Processing , Digital Signal Processing, QT 5.13, Window 7 , Visual Studio 2012 , Visual Studio 2008

Project Details	Larsen & Toubro
Clients Details	CITI Corp(Apr-2015,Jun-2016)

Projects	OAsys	
This Project used C/C++, Sybase IQ, shell scripting technology for accepting trade from front office. it is a software system that supports the operational activities that are involved in a Global OTC Derivatives business.		
 Role – Development & Unit Testing Responsibilities Prepared jobs solutions for system. Requirement Gathering and development accordingly. 		
Developed, implementation, testing, design, analysis. Environment. C/C++, Unix, Sybase on Solaris and Linux platform		

Project Details	Tata Consultancy Services		
Clients Details	CCIL(Jul-2012, Mar-2015)		
Projects	Central Communication System(CCS)		
This Project used multithreaded architecture using c/c++, pro*c, IPC mechanism, TCP/IP protocol to facilitate CCIL to transmission of binary data Unix system to window system. This transmission carries forex forward trade for exposure check. Involved in POC for IN-memory implementation for RMS exposure check.			
 Role – Technical Design & Developr Technical Spec Writing, Req Prepared architecting solutio Developed, implementation, Environment C/C++, Unix, pro*c, TCP/IP,IPC, PL/ 	uirement Gathering. ons for system architecture design. testing, design, analysis.		
Projects	System Monitoring System(SMS)		
Project used to Automated Scheduled monitor all the system of CCIL. Monitoring includes CPU, MEMORY, Disk Utilization, Channel Status, Current Depth Status, Message and File.			
Role – Technical Design & Development Responsibilities			
• Full Module Design & Development, Customization, Installation, Configuration,			
Troubleshooting, Technical Support and Customer			

Support, Technical Supervision, User Support, System Testing and

• Production Deployment.

• Interact with users to provide application support and to define requirements, prepare technical.

• specifications and necessary documentation. Migration/Upgrade of the whole existing module.

Environment C/C++, Proc*C, Shell Scripting

Education

MBA Banking & Finance (2019-2021)	IGNOU
B. Tech Computer Science & Engineering (2008-2012)	BIT Sindri Dhanbad
Intermediate Science Math's (2005-2007)	ST. Xavier College Ranchi
Matriculation (2005)	St. Aloysius High School Ranchi

DECLARTION

I, hereby, certify that the afore-mentioned information is true to the best of my Knowledge and belief.

PLACE: Sweden

DATE:

AKHIL BHARTIYA